Basic Assembly Language

Operations – actions the computer carries out
Operands – data acted on
Operands – the data acted on by instructions

• Where are they?

Processor has internal locations for storing data or addresses: registers
Each register holds a standard unit: double word = 64 bits
Limited number of registers: 32 for ARMv8

Memory is separate from processor
Has almost unlimited space for programs and data
Addressed like a giant array, each byte is addressed
Instructions – a load/store architecture

• Instructions for arithmetic and logical operations only work on values in registers or given as part of the instruction (immediate)
  • Simpler instructions
  • Faster access to data
  • For ARMv8 designated X0, X1, X2, ... X31

• Separate instructions move data from/to memory locations
  • Load bring data from a memory location to a register
  • Store sends data to a memory location from a register
Arithmetic instructions

• Take three parameters:
  • Destination for result (always a register)
  • Two operands
    • First is always a register
    • Second may be a register or immediate

Example: add X11, X9, X10
Takes the values found in X9 and X10, sums them and puts the result into X11

Example sub X19, X10, #8
Takes the value in X10, subtracts the value 8, places result into X19
More complex expressions take more instructions to complete

Example: \( g = a + b + c + d \)

Assume \( a \) is in \( X9 \), \( b \) in \( X10 \), \( c \) in \( X11 \), \( d \) in \( X12 \), \( g \) in \( X19 \)

\[
\text{add X19, X9, X10} \quad \text{\( \text{X19} = a + b \)}
\]

\[
\text{add X19, X19, X11} \quad \text{\( \text{X19} = (a+b) + c \)}
\]

\[
\text{add X19, X19, X12} \quad \text{\( \text{X19} = ((a+b) + c) + d \)}
\]

Note: “//” indicates the rest of the line is a comment ignored by system
Can you do this?

Example: $g = (a - b) + (c - d)$
Assume $a$ is in $X9$, $b$ in $X10$, $c$ in $X11$, $d$ in $X12$, $g$ in $X19$
Answer

Example: \( g = (a - b) + (c - d) \)

Assume \( a \) is in \( X9 \), \( b \) in \( X10 \), \( c \) in \( X11 \), \( d \) in \( X12 \), \( g \) in \( X19 \)

\[
\text{sub X13, X9, X10} \quad \text{// X13 = a - b} \\
\text{sub X14, X11, X12} \quad \text{// X14 = c - d} \\
\text{add X19, X13, X14} \quad \text{// X19 = (a - b) + (c - d)}
\]
What if information is in memory

Suppose we have an array, A[], in memory
Address   value

10000     35
10008     21
10016     -12

10000 is the “base address” of the array
Since each data item is 64 bits and memory is addressed by bytes (8 bits), each item in the array has the address increased by 8
Accessing the array A

First assume that we have 1000 placed in register X20 (the base address)

Then
ldur X9, [X20, #0] puts the first array value into register X9
ldur X10, [X20, #8] puts the second array value into register X10
add X11, X9, X10 would add the two array values
stur X11, [X20, #16] would store the result into the memory for the third array element (overwriting any value there before)